

**ABSTRACT OF THE DISCLOSURE**

A video sampling circuit of a liquid crystal display with a merit of feed-through voltage reduction was disclosed. The sampling circuit comprises a first thin film transistor (TFT) and a counteracting device. The first TFT has a first electrode to receive the analog signal, a control electrode to receive the clock signal, and a second electrode, and samples the analog signal when the clock signal is at a first logic level. The counteracting device is coupled to the second electrode. When the clock signal is changed from the first logic level to a second logic level, a feed-through voltage drop caused by a parasitic capacitor between the second electrode and the control electrode of the first TFT is reduced.